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(71) Applicant (for all designated States except US): SAM-SUNG ELECTRONICS CO., LTD. [KR/KR]; 416, Maetan-dong, Paldal-ku, Suwon-city, Kyungki-do 442-370 (KR).

(72) Inventors; and

(75) Inventors/Applicants (for US only): YU, Seung-Hee [KR/KR]; 140-501 Hwanggol maoul Jookong Apt., Youngtong-dong, Paldal-ku, Suwon-city, Kyungki-do 442-739 (KR). HONG, Mun-Pyo [KR/KR]; 401-2202 Purun-maoul Ssangyong Apt., Sunae-dong, Bundang-ku, Seongnam-city, Kyungki-do 463-781 (KR). RHO, Soo-Guy [KR/KR]; 140-501 Hwanggol maoul Jookong Apt., Youngtong-dong, Paldal-ku, Suwon-city, Kyungki-do 442-739 (KR). ROH, Nam-Seok [KR/KR]; 607-703 Hyojachon Apt., 308, Seohyun-dong, Bundang-ku, Kyungki-do 463-050 (KR). SONG, Keun-Kyu [KR/KR]; 508-51, Sungnam-dong, Dong-ku, Daejeon-city 300-180 (KR). CHOE, Hee-Hwan [KR/KR]; 158-507 Jookong

Apt., Kuwol-dong, Namdong-ku, Incheon-city 405-761 (KR). KIM, Bo-Sung [KR/KR]; 301 Songhyun Villa, 100-524, Bongcheon 6-dong, Kwanak-ku, Suwon-city, Seoul 151-810 (KR). KIM, Sang-Gab [KR/KR]; 301-306 Samik Apt., 15, Myungil-dong, Kangdong-ku, Seoul 134-070 (KR). KANG, Sung-Chul [KR/KR]; 164-1001 Hyundai Sungwoo 2-cha Apt., Sanghyun-ri, Suji-eup, Yongin-city, Kyungki-do 449-843 (KR). PARK, Hong-Sick [KR/KR]; 1001 Sinmyung Apt., 384-2, Gogul-ri, Kiheung-eup, Yongin-city, Kyungki-do 449-900 (KR).

(74) Agent: YOU ME PATENT & LAW FIRM; 825-33 Teheran Bldg., Yoksam-dong, Kangnam-ku, Seoul 135-080 (KR).

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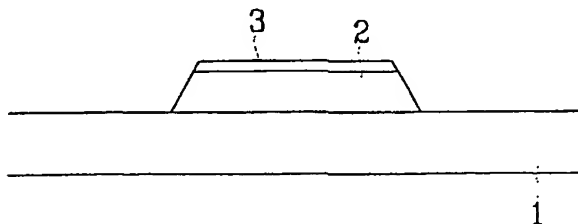
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(54) Title: A WIRE FOR A DISPLAY DEVICE, A METHOD FOR MANUFACTURING THE SAME, A THIN FILM TRANSISTOR ARRAY PANEL INCLUDING THE WIRE, AND A METHOD FOR MANUFACTURING THE SAME

(57) Abstract: First, a Cr film and a CrOx film are deposited and patterned using an etchant including 8-12% Ce(NH₄)₂(NO₃)₆, 10-20% NH₃ and remaining ultra pure water to form a gate wire including a plurality of gate lines, a plurality of gate electrodes and a plurality of gate pads. Next, a gate insulating film, a semiconductor layer and an ohmic contact layer are formed in sequence. A Cr film and CrOx film are deposited in sequence and patterned using an etchant including 8-12% Ce(NH₄)₂(NO₃)₆, 10-20% NH₃ and remaining ultra pure water to form a data wire including a plurality of data lines, a plurality of source electrodes, a

plurality of drain electrodes and a plurality of data pads. A passivation layer is deposited and patterned to form a plurality of contact holes respectively exposing the drain electrodes, the gate pads and the data pads. A transparent conductive material or a reflective conductive material is deposited and patterned to form a plurality of pixel electrodes, a plurality of subsidiary gate pads and a plurality of subsidiary data pads electrically connected to the drain electrodes, the gate pads and the data pads, respectively. The gate lines and the data lines with low reflectance are used as a light-blocking film for blocking the light leakage between the pixel areas, and do not increase the black brightness. Accordingly, a separate black matrix need not be provided on the color filter panel, thereby securing both aperture ratio of the pixel and high contrast ratio.

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